

I Claim:

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7. The method of claim 5, wherein the first video memory and second video memory are accessed by a direct memory access (DMA) controller on the second VGA.

8. The method of claim 1, wherein the first VGA is a primary VGA, and the second VGA is a secondary VGA.

9. The method of claim 1, wherein the first VGA is a secondary VGA, and the second VGA is a primary VGA.

10. The method of claim 1, wherein the first VGA and the second VGA are part of a video wall such that the first frame of active video is displayed across multiple displays simultaneously.

11. The method of claim 1 further comprising the steps of:
receiving at the second VGA a second frame of active video from a second video source;
rendering at least a portion of the second frame of video at the first VGA.

12. The method of claim 1, wherein the first control signal is a signal specifying a window location for displaying the active video.

13. The method of claim 12 further comprising the step of storing the window location in a preference file.

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14. A processing system for executing instructions, the processor system comprising instructions for:

monitoring the location of an active video window;
 storing active video data at first video memory;
 sending the active video data from the first video memory to a second video memory
 when the location of the active video window is associated with the second
 video memory.

15. A method of displaying active video on a computer system, the method comprising the steps of:

receiving at a first video graphics adapter (VGA) a first frame of active video from a video
 source
 displaying at least a first portion of the first frame of video at a second VGA in response to a
 second control signal.

16. The method of claim 15, wherein the method further comprises the video source being a
 video decoder.

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17. The method of claim 16, wherein the video decoder is for decoding a compressed video
 signal.

18. The method of claim 16, wherein the method further comprises the video source sending the
 first frame of data over a bus local to the first VGA.

- 1 19. The method of claim 15, wherein the method further comprises storing the first frame of
 2 active video in a video memory associated with the first VGA.

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 1 20. The method of claim 15, wherein the method further comprises the video source being a
 2 television signal.

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